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Julie Mattheus

(Depositor's name)

(Signature)

August 25, 2004

(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,109	07/29/2003	Ping-Chen Liu	X-1174 US	5908

TITLE OF INVENTION: COLUMN REDUNDANCY SCHEME FOR SERIALLY PROGRAMMABLE INTEGRATED CIRCUITS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	09/07/2004
EXAMINER	ART UNIT		CLASS-SUBCLASS		
DINH, SON T	2824		365-200000		

Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1 John Kubodera2 Edel M. Young3 Justin Liu

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(A) NAME OF ASSIGNEE

Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124

Please check the appropriate assignee category or categories (will not be printed on the patent):  individual  corporation or other private group entity  government

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(Date)

Justin Liu, 51-959 August 25, 2004

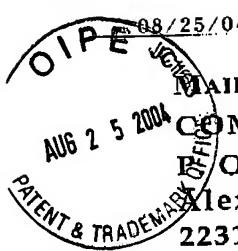
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P. O. Box 1450  
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22313-1450

Inventor(s): Ping-Chen Liu et al.

Assignee: XILINX, INC.

conf.no. 5908

Serial No.: 10/629,109

Filed: July 29, 2003

Title: Column Redundancy Scheme for Serially Programmable Integrated Circuits

Docket No.: X-1174 US

Enclosed: Return Receipt Postcard  
Part B. - Fee Transmittal

Date: August 25, 2004

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